

DIPSWTCH.XLS

**Older 8300 and 7700B**

F = MHz  
 / 50 KHz  
 N = -17530  
 On = 0  
 Off = 1

Pos. **SW1**

|   |          |      |
|---|----------|------|
| 8 | 256 = 0  | #### |
| 7 | 512 = 0  | #### |
| 6 | 1024 = 0 | #### |
| 5 | 2048 = 0 | #### |
| 4 | 8 = 0    | #### |
| 3 | 4 = 0    | #### |
| 2 | 2 = 0    | #### |
| 1 | 1 = 0    | #### |

Pos. **SW2**

|   |         |      |
|---|---------|------|
| 4 | 128 = 0 | #### |
| 3 | 64 = 0  | #### |
| 2 | 32 = 0  | #### |
| 1 | 16 = 0  | #### |

**Newer 8300 and 7700B STL TX**

F = MHz  
 2Fvco1 = -1753 MHz  
 N = -1753  
 A = 0  
 On = 0  
 Off = 1

Pos. **S1**

|   |         |      |
|---|---------|------|
| 8 | 128 = 0 | #### |
| 7 | 64 = 0  | #### |
| 6 | 32 = 0  | #### |
| 5 | 16 = 0  | #### |
| 4 | 8 = 0   | #### |
| 3 | 4 = 0   | #### |
| 2 | 2 = 0   | #### |
| 1 | 1 = 0   | #### |

Pos. **S2**

|   |         |      |
|---|---------|------|
| 8 | 32 = 0  | 0    |
| 7 | 16 = 0  | 0    |
| 6 | 8 = 0   | 0    |
| 5 | 4 = 0   | 0    |
| 4 | 2 = 0   | 0    |
| 3 | 1 = 0   | 0    |
| 2 | 512 = 0 | #### |
| 1 | 256 = 0 | #### |

**9100,9100A**

**STL TX**

F = MHz  
 2Fvco1 = -1740.8 MHz  
 N = -1741  
 A = 8  
 On = 0  
 Off = 1

Pos. **S1**

|   |         |      |
|---|---------|------|
| 8 | 128 = 0 | #### |
| 7 | 64 = 0  | #### |
| 6 | 32 = 0  | #### |
| 5 | 16 = 0  | #### |
| 4 | 8 = 0   | #### |
| 3 | 4 = 0   | #### |
| 2 | 2 = 0   | #### |
| 1 | 1 = 0   | #### |

Pos. **S2**

|   |         |      |
|---|---------|------|
| 8 | 32 = 0  | 8    |
| 7 | 16 = 0  | 8    |
| 6 | 8 = 1   | 0    |
| 5 | 4 = 0   | 0    |
| 4 | 2 = 0   | 0    |
| 3 | 1 = 0   | 0    |
| 2 | 512 = 0 | #### |
| 1 | 256 = 0 | #### |

**9107,9107A, 9107B STL RX**

**940 - 960 MHz-**

F = MHz 832  
 F1 = -902 MHz On = 0  
 -1804 Off = 1  
 N = -1804  
 A = 0

Pos. **S1**

|   |         |      |
|---|---------|------|
| 8 | 128 = 0 | #### |
| 7 | 64 = 0  | #### |
| 6 | 32 = 0  | #### |
| 5 | 16 = 0  | #### |
| 4 | 8 = 0   | #### |
| 3 | 4 = 0   | #### |
| 2 | 2 = 0   | #### |
| 1 | 1 = 0   | #### |

Pos. **S2**

|   |         |      |
|---|---------|------|
| 8 | 32 = 0  | 0    |
| 7 | 16 = 0  | 0    |
| 6 | 8 = 0   | 0    |
| 5 | 4 = 0   | 0    |
| 4 | 2 = 0   | 0    |
| 3 | 1 = 0   | 0    |
| 2 | 512 = 0 | #### |
| 1 | 256 = 0 | #### |